ABSTRACT OF THE DISCLOSURE

A discharging apparatus for a liquid crystal display is provided for substantially reducing a residual image upon power-off. In the apparatus, a gate driver integrated circuit selectively applies first and second gate voltages to gate lines of the display. A discharge circuit is coupled to the gate driver integrated circuit and senses a power-off state of a power supply line. When a power-off state is sensed, a short-circuit if formed between the first gate voltage supply line and the second gate voltage supply line, thereby discharging voltages on the gate lines. Accordingly, a gate low voltage relative gate high (pixel turn-on) voltage is discharged upon power-off to define a discharge path via the gate line, thereby rapidly discharging electric charges charged in the liquid crystal display panel.

1-WA/1620636.1

13